

INFORMATION DISCLOSURE STATEMENT				Atty. Docket No. 1582-US		Serial No. Unknown	
				Applicant Breger et al			
				Filing Date 2/28/2002		Group Unknown	

PTO
 10/090585
 02/28/02

U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date if appropriate
<i>He</i>	AA	6,256,237	7/3/2001	Ho et al	365	200	
	AB						
	AC						
	AD						
	AE						
	AF						

FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AG							
	AH							
	AI							

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
<i>bu</i>	AJ	A Post-Package Bit-Repair Scheme Using Static Latches with Bipolar-Voltage Programmable Antifuse Circuit for High-Density DRAMs, Kyeong-Sik Min et al, 2001 Symposium on VLSI Circuits Digest of Technical Papers, 2001, 2 pages. (no month)
	AK	
	AL	
	A M	
	AN	
	AO	

Examiner: <i>John Williams</i>	Date Considered: <i>9-4-03</i>
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Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.